

501.42810X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

T. HONMURA

Application No.:

10/603,797

Filed:

June 26, 2003

For:

SEMICONDUCTOR INTEGRATED CIRCUITS

Art Unit:

2182

Examiner:

T. NGUYEN

AMENDMENT AS SUBMISSION UNDER 37 CFR §1.114

MS: RCE

December 13, 2006

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 CFR §1.114, and in response to the Final Office Action dated June 20, 2006, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages (noting the timely filing of a Request for Continued Examination and a Petition for Extension of Time on even date herewith:

Amendments to the Claims;

Remarks are included following the amendments.